

LAST: [02/25/2009] semiconductors resistors and US 588875 A

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Jan, 2005

	U	Inventor	Document ID	Issue P	Title	Current	Current X	Retrieval	S	C	P	Image	Do	P
1	<input checked="" type="checkbox"/>	Lasky, Jero	US 5888875 A	1999	8	Diffusion resistor structure with silicide	438/38	257/E21.3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 58888
2	<input checked="" type="checkbox"/>	Usuki, Hide	US 5377140 A	1994	1	Cell for random access memory	385/15	257/538	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 53771
3	<input checked="" type="checkbox"/>	Takeda, Ka	US 4898837 A	1990	1	Method of fabricating a semiconductor	438/33	257/539	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 48988
4	<input checked="" type="checkbox"/>	Langdon, J	US 3728662 A	1973	5	SEMICONDUCTOR RESISTOR	257/53	148/DIG.3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 37286
5	<input checked="" type="checkbox"/>	Liou, Jhyy	US 20040217379 A	2004	1	Compact mask programmable rom	257/20		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040
6	<input checked="" type="checkbox"/>	Voldman, S	US 6465870 B2	2002	1	ESD robust silicon germanium transist	257/56	257/197	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 64658
7	<input checked="" type="checkbox"/>	Yu, Allen S	US 6287922 B1	2001	4	Method for fabricating graded LDD tra	438/29	257/E21.2	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 62879
8	<input checked="" type="checkbox"/>	Sato, Tetsu	US 20040232453 A	2004	3	Method for manufacturing a semicond	257/22		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040
9	<input checked="" type="checkbox"/>	Hall, Jeffer	US 6362067 B1	2002	6	Accurate self-aligned resistor structur	438/38	257/E27.0	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 63620
10	<input checked="" type="checkbox"/>	Komori, Shi	US 20040248358 A	2004	2	MANUFACTURING METHOD OF A SE	438/23		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040